

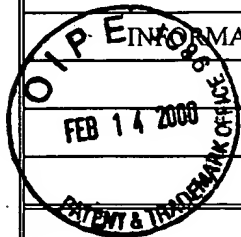
Part 2 #2

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-7086 US		09/347,106	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)					Applicant		<div style="border: 1px solid black; padding: 5px; display: inline-block;"> RECEIVED NOV - 9 1999 Group 2700 </div>	
					Hronik, Stanley A.			
					Filing Date		Group	
					July 2, 1999		Unknown	

U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
<div style="font-size: 2em; font-family: cursive;">MPT</div>	AA	4,882,709	11/21/89	Wyland	365	189.02		
	AB	5,515,325	05/07/96	Wada	365	189.01		
	AC	5,526,320	06/11/96	Zagar et al.	365	233.5		
	AD	5,546,344	08/13/96	Fawcett	365	189.05		
	AE	5,568,430	10/22/96	Ting	365	189.05		
	AF	5,577,236	11/19/96	Johnson et al.	713 395	40 0551		
	AG	5,617,362	04/01/97	Mori et al.	365	189.05		
	AH	5,644,729	07/01/97	Amini et al.	710 395	128 250		
	AI	5,652,724	07/29/97	Manning	365	189.05		
	AJ	5,659,696	08/19/97	Amini et al.	711 395	202 412		
	AK	5,663,901	09/02/97	Wallace et al.	365	52		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
<div style="font-size: 2em; font-family: cursive;">MPT</div>	AQ	Gibson, Glenn A., <u>Computer Systems--Concepts and Design</u> , Prentice Hall, Inc., Englewood Cliffs, New Jersey, 1991, pp. 366-369 and 452-493						
	AR	Prince, Betty, <u>Semiconductor Memories</u> , Second Edition, (John Wiley & Sons ed., 1991) (1983) pp. 467-472						
	AS	"64K x 32, 3.3V Synchronous SRAM With Pipelined Outputs and Interleaved/Linear Burst Counter, Preliminary IDT71V632," Integrated Device Technology, Inc., Product Information, March 1997, 14 pages						
Examiner		<div style="font-size: 1.5em; font-family: cursive;">MPT</div>						
		Date Considered		12/5/00				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
MDA	AA	5,675,549	10/07/97	Ong et al.	365	233.5		
	AB	5,699,317	12/16/97	Sartore et al.	365	230.06		
	AC	5,828,606	10/27/98	Mick	365	189.05		
	AD	5,838,631	11/17/98	Mick	365	233		
	AE	5,841,732	11/24/98	Mick	365	233		
	AF	5,875,151	02/23/99	Mick	365	233		
	AG							
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	AK							
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	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
MDA	AQ	"64K x 32, 3.3V Synchronous SRAM With Flow-Through Outputs-Preliminary IDT71V633," Integrated Device Technology, Inc., Product Information, April 1997, 15 pages						
	AR							
	AS							
Examiner	<i>[Signature]</i>			Date Considered 12/15/00				
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicant(s)			
(Use several sheets if necessary)						Hronik, Stanley A.			
Filing Date						Group			
July 2, 1999						275			
U.S. Patent Documents									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
MAA	AA	5,513,327	04/30/1996	Farmwald et al.	710 395	129 309			
MAA	AB	5,915,105	06/22/1999	Farmwald et al.	710 395	129 309			
	AC								
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